

Electronic Patent Application Fee Transmittal

Application Number:	10564473			
Filing Date:	13-Jan-2006			
Title of Invention:	Delay time correction circuit, video data processing circuit, and flat display device			
First Named Inventor/Applicant Name:	Masaki Murase			
Filer:	Brian Keith Dutton			
Attorney Docket Number:	SON-3058			

Filed as Large Entity

U.S. National Stage under 35 USC 371 Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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Basic Filing:

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Filing a brief in support of an appeal	1402	1	540	540
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Post-Allowance-and-Post-Issuance:

Extension-of-Time:

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				540